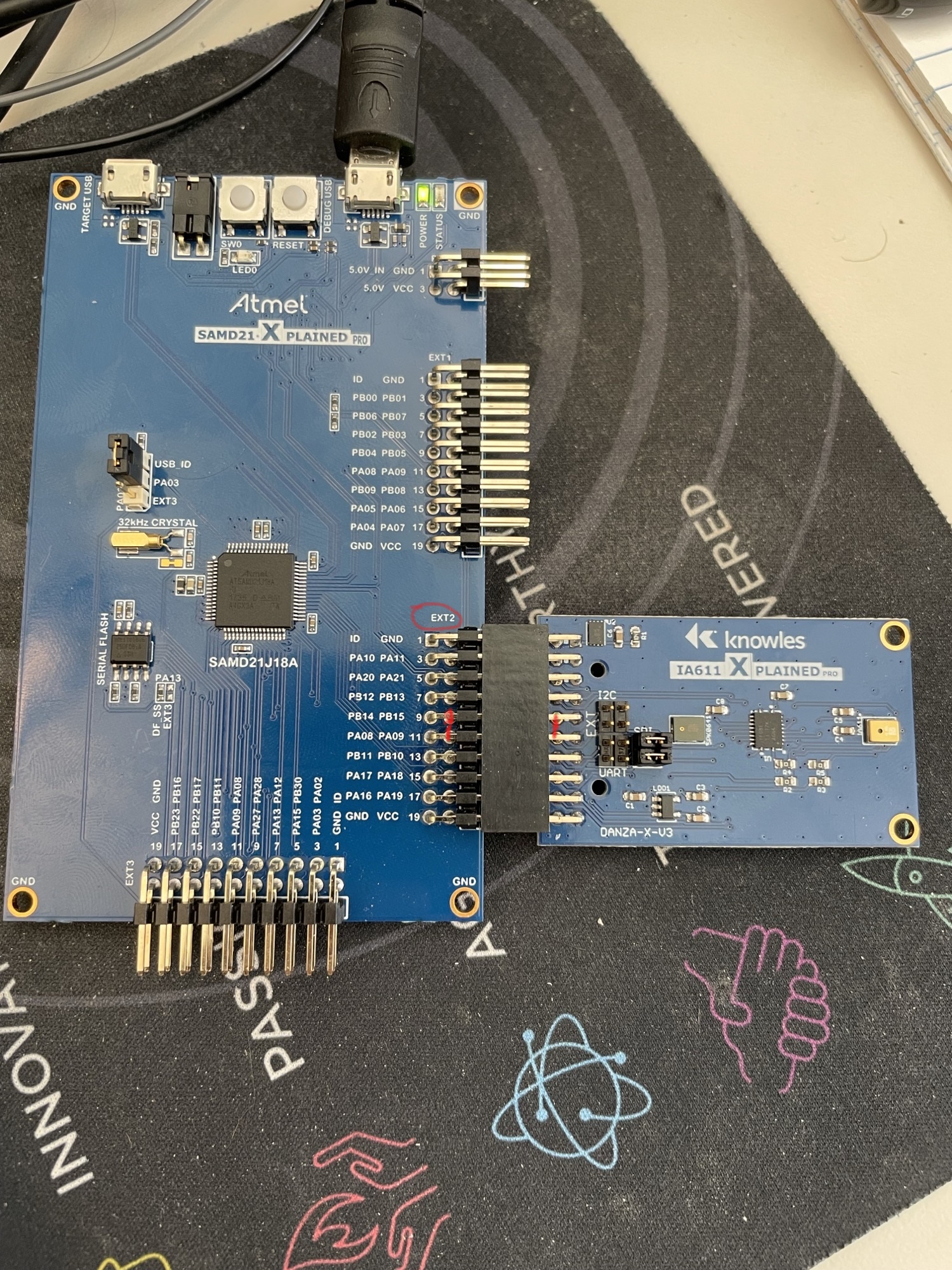
# Atmel Danza Board HW Change

* The Atmel board can only output clock on EXT2 and EXT3 so the Danza will plug in EXT2
* Next you will need to short PB15 and PB13 on the Atmel board or pins 8 and 10 on the Danza board in order for the clock to go to P0 on IA611



# Clock and SPI changes

* In the provided conf\_clocks.h file, copy and paste all of the code over the current conf\_clocks.h file. This will overwrite with the output clock at 3.072M
* The function config\_outClk() will enable the output clock on PB15. I have this defined in main.c
* Lastly, copy and paste the IA61x\_samd21\_VQ\_spi.h file and overwrite the existing. This will change the SPI output to EXT2 instead of EXT1

static void config\_outClk(void)

{

struct system\_pinmux\_config mux\_conf;

system\_pinmux\_get\_config\_defaults(&mux\_conf);

mux\_conf.mux\_position = PINMUX\_PB15H\_GCLK\_IO1;

mux\_conf.direction = SYSTEM\_PINMUX\_PIN\_DIR\_OUTPUT;

system\_pinmux\_pin\_set\_config(PIN\_PB15H\_GCLK\_IO1, &mux\_conf);

}

// Call this function to start the output clock on PB15